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RESPONSE UNDER 37 CFR 1.116

EXPEDITED PROCEDURE

EXAMINING GROUP 2183

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MAY 25 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hoyle et al

Art Unit: 2183

Serial No.: 09/703,144

Examiner: Barry J. O'Brien

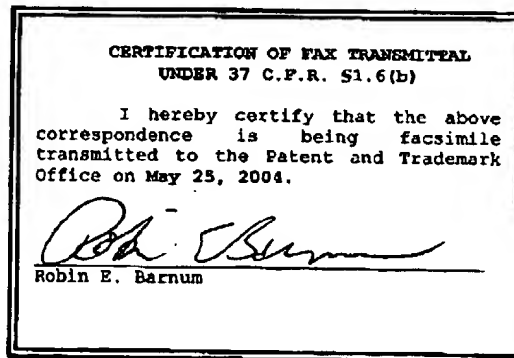
Filed: October 31, 2000

Docket: TI-30564

For: MICROPROCESSOR WITH BRANCH-DECREMENT INSTRUCTION

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated March 24, 2004. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows:

- 1 -

Do Not Enter
BSO 6/2/04